

Center for VLSI Design

Dr. Kent F. Smith/University of Utah/SLC, Utah

Established center in 1991. A new technology called Path Programmable Logic (PPL) is an innovative design approach for compact integrated circuits. This technology has been developed in response to an identified marked niche for a less expensive, more user-friendly design program. The ability to interface PPL with other commercial software packages makes this design program attractive to smaller engineering firms needing custom integrated circuits.

<u>Overview</u>	<u>Technologies</u>	<u>Status</u>	<u>Economic Impact</u>
Current State Contract \$100,000 Matching Funds \$500,000 Cumulative 500,000	*Physical placement of logic software, libraries and cell libraries.	*Transfer of source code of licensed technology to private Utah company.	*Joint venture was formed with a European company to sell center products in Europe.
Industry Jobs Created 4 Center Related Jobs 21	*Interfaces to 3rd party software.	*Development of new tools and libraries in CMOS, GaAs and HBT technologies.	*First generation sales of software beginning.
Benefiting Utah Co.'s 1 Spin-off companies 1			*Beginning discussions with companies in the Pacific Rim.
Patents Applied - Patents Issued -			
License Agreements 1			